

Low Power Digital CMOS Design

By Anantha P. Chandrakasan

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Anantha P. Chandrakasan is the author of Low Power Digital CMOS Design (0.0 avg rating, 0 ratings, 0 reviews, published 1995), Technologies for Wireless http://www.goodreads.com/author/show/564889.Anantha_P_Chandrakasan

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Prof. Anantha P. Chandrakasan serves as the Chief Scientist at Engim, Inc. Prof Chandrakasan is one of the world's leading experts on low power VLSI design.

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